

# Space vector PWM Technique for 3phase voltage source inverter using Artificial Neural Network

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**Abstract**— *Space vector pulse width modulation (SVPWM) is an optimum pulse width modulation technique for an inverter used in a variable frequency drive applications. It is computationally rigorous and hence limits the inverter switching frequency. A neural network has the advantage of very fast implementation of an SVPWM algorithm that can increase the inverter switching frequency. This paper proposes a neural Network based SVPWM technique for a three-phase voltage source inverter in under modulation region. The scheme has been simulated and implemented on a V/Hz controlled 5-hp, 50-Hz, and 230V induction motor drive. The simulation results are given to validate the performances of the drive with artificial neural network based SVPWM.*

**Index Terms**—Space vector pulse width modulation, neural network, voltage source inverter.

## I. INTRODUCTION

Variable frequency ac drives are increasingly replacing dc drives in a number of industrial applications due to advantages in size, reliability and efficiency. One of the main components of an ac drive is power electronic converter in the form of voltage source inverter that takes dc voltage input (may be from a rectifier) and produces a sinusoidal ac waveform. This in turn is fed to the ac electric motor. The fundamental frequency of this waveform is adjusted to produce the desired speed. In modern electric drives the modulation of switching is carried out to achieve the required ac wave shape. This is met by incorporating the duration of ‘ON’ & ‘OFF’ methods [1, 2].

The techniques to get the duration of ON interval for a particular switch depend upon the control logic or PWM technique to be adopted. In a PWM scheme the output voltage and frequency can be controlled with the help of the switching inside the inverter. The switch may be MOSFET, IGBT etc with anti parallel connected diodes. Different PWM schemes such as sinusoidal PWM compares a high frequency triangular carrier with three sinusoidal reference signals, known as the modulating signals, to generate the gating signals for the inverter switches but having a disadvantage that it contains third harmonic in output[3]. To the cancellation of the third-harmonic components and better utilization of the dc supply, the third harmonic injection PWM scheme is preferred in three-phase applications. Space vector modulation technique has advantage of an optimal output and also reduces harmonic content of the output voltage/current [4]. Space vector PWM (SVPWM) has the advantages of lower harmonics and a higher modulation index in addition to the features of complete digital

implementation by a single chip microprocessor, because of its flexibility of manipulation, SVPWM has increasing application in power converters and motor control.

The application of artificial neural network (ANN) is recently growing in power electronic systems. A feed forward ANN implements nonlinear input–output mapping. The computational delay of this mapping becomes negligible, if parallel architecture of the network is implemented by an application-specific integrated circuit (ASIC) chip [5]. A feed forward carrier-based PWM technique, Such as SVM, can also be looked upon as a nonlinear mapping phenomenon where the command phase voltages are sampled at the input and the corresponding pulse width patterns are established at the output. Therefore, it appears logical that a back propagation-type ANN that has high computational capability can implement an SVM algorithm. The ANN can be conveniently trained offline with the data generated by calculation of the SVM algorithm. ANN has inherent learning capability that can give improved precision by interpolation unlike the standard lookup table method [5].

The ANN-based SVPWM of a voltage-fed inverter can also use competitive neural network architecture to identify the inverter switching states and the corresponding voltage magnitudes for the impressed command voltage. This paper describes feed- forward ANN-based SVPWM that covers linear modulation region. In the beginning, the SVPWM theory has been briefly reviewed with mathematical analysis for the linear modulation range. Then equations for algorithms have been developed in detail. A back propagation type feed forward ANN is trained offline with the data generated by this simple algorithm [5].

## II. SPACE VECTOR PWM – A REVIEW

This section is devoted to the development of Space vector PWM for a two-level voltage source inverter in linear region of operation [6]. As seen from Fig 1, there are six switching devices and only three of them are independent as the operation of two power switches of the same leg are complimentary. The combination of these three switching states gives out eight possible space voltage vectors. The space vectors forms a hexagon with 6 distinct sectors, each spanning 60 degrees in space. At any instant of time, the inverter can produce only one space vector. In space vector PWM a set of three vectors (two active and a zero) can be selected to synthesize the desired voltage in each switching period. All of the eight modes are shown in Table.1.

Out of eight topologies six (states 1-6) produce a non-zero output voltage and are known as active voltage vectors and

the remaining two topologies (states 0 and 7) produce zero output voltage (when the motor is shorted through the upper or lower transistors) and are known as zero voltage vectors, various possible switching states are shown in Fig 2.

Space vector is defined as [6],

$$\underline{v}_s^* = \frac{2}{3}(v_a + \underline{a}v_b + \underline{a}^2v_c) \quad (1)$$

Where  $\underline{a} = \exp(j2\pi/3)$ .

The space vector is a simultaneous representation of all the three-phase quantities [14]. It is a complex variable and is function of time in contrast to the phasors. Phase-to-neutral voltages of a star-connected load are most easily found by defining a voltage difference between the star point  $n$  of the load and the negative rail of the dc bus  $N$ . The following correlation then holds true:

$$\begin{aligned} v_A &= v_a + v_{nN} \\ v_B &= v_b + v_{nN} \\ v_C &= v_c + v_{nN} \end{aligned} \quad (2)$$

Since the phase voltages in a star connected load sum to zero, summation of equation (2) yields

$$v_{nN} = (1/3)(v_A + v_B + v_C) \quad (3)$$

Substitution of (3) into (2) yields phase-to-neutral voltages of the load in the following form:

$$\begin{aligned} v_a &= (2/3)v_A - (1/3)(v_B + v_C) \\ v_b &= (2/3)v_B - (1/3)(v_A + v_C) \\ v_c &= (2/3)v_C - (1/3)(v_B + v_A) \end{aligned} \quad (4)$$

Phase voltages are summarized and their corresponding space vectors are listed in Table 1. The eight vectors including the zero voltage vectors can be expressed geometrically as shown in fig.3. Each of the space vectors, in the diagram represent the six voltage steps developed by the inverter with the zero voltages  $V_0$  (0 0 0) and  $V_7$  (1 1 1) located at the origin. Space Vector PWM require to averaging of the adjacent vectors in each sector. Two adjacent vectors and zero vectors are used to synthesis the input reference determined from Fig.4 for sector I. Using the appropriate PWM signals a vector is produced that transitions smoothly between sectors and thus provide sinusoidal line to line voltages to the motor.

In order to generate the PWM signals that produces the rotating vector. The PWM time intervals for each sector is determined from Fig. 4 for sector I as

Along real axis:

$$V_1 \frac{T_1}{T_s} + (V_2 \cos 60) \frac{T_2}{T_s} = mV_s \cos \alpha \quad (5)$$

Along imaginary axis:

$$0 + (V_2 \sin 60) \frac{T_2}{T_s} = mV_s \sin \alpha \quad (6)$$

Solving equations (5) and (6)

$$T_1 = T_s \frac{mV_s \sin(60 - \alpha)}{V_1 \sin 60}$$

$$T_1 = T_s \frac{m \frac{1}{2} V_{DC} \sin(60 - \alpha)}{\frac{2}{3} V_{DC} \sin 60}$$

$$T_1 = \frac{\sqrt{3}}{2} mT_s \sin(60 - \alpha) \quad (7)$$

$$T_2 = T_s \frac{mV_s \sin \alpha}{V_2 \sin 60}$$

$$T_2 = \frac{\sqrt{3}}{2} mT_s \sin(\alpha) \quad (8)$$

$$T_0 + T_7 = T_s - (T_1 + T_2) \quad (9)$$

Generalizing the time expressions gives

$$T_1 = \frac{\sqrt{3}}{2} mT_s \sin\left(\frac{k\pi}{3} - \alpha\right) \quad (10)$$

$$T_2 = \frac{\sqrt{3}}{2} mT_s \sin\left(\frac{(k-1)\pi}{3} - \alpha\right); k=1, 2, 3... \quad (11)$$

The time of application of active and zero space vectors for all six sectors are given in Table 2 [13]. The periods  $T_1$ ,  $T_2$  and  $T_0$  depends only on the reference vector amplitude  $V_s$  and the angle ' $\alpha$ '. This shows that the period  $T_1$ ,  $T_2$  and  $T_0$  are the same in all sectors for the same  $V_s$  and ' $\alpha$ ' position. In the under modulation region, the vector  $V_s$  always remains within the hexagon. The mode ends in the upper limit when  $V_s$  describes the inscribed circle of the hexagon. Modulation Index  $MI(m)$  is given by

$$m = \frac{V_s^*}{V_{1Sixstep}}$$

Where,  $V_s$  = input reference vector magnitude

$V_{1Sixstep}$  = fundamental peak value  $\frac{2V_{DC}}{\pi}$  of the six step output.

The maximum value of input reference is the radius of largest circle inscribed in the hexagon given by

$$V_s^* = \frac{2}{3} V_{DC} \cos(30^\circ) = 0.577V_{DC}$$

Therefore, maximum modulation index

$$m = \frac{V_s^*}{V_{1sw}} = \frac{0.577V_{DC}}{\frac{2}{\pi} V_{DC}} = 0.907 \quad (12)$$

This means that 90.7% of the fundamental of the six step wave is available in the linear region, compared to 78.55% in the sinusoidal PWM [7].

### III. NEURAL NETWORK BASED SPACE VECTOR PWM

The first step that involves in the implementation of neural network based space vector PWM of a voltage source inverter is generation of training data. The training data is generated by using eqs. (1), (2), (3).the angle subnet is trained with an angle interval of  $2^\circ$  in the range of  $0 - 360^\circ$ . The phase-A turn ON time can be expressed as

$$T_{A=ON} = \begin{cases} \frac{t_0}{4} = \frac{T_s}{4} + K.V^* \left[ -\sin\left(\frac{\pi}{3} - \alpha^*\right) - \sin(\alpha^*) \right], S = 1,6 \\ \frac{t_0}{2} + t_b = \frac{T_s}{4} + K.V^* \left[ -\sin\left(\frac{\pi}{3} - \alpha^*\right) + \sin(\alpha^*) \right], S = 2 \\ \frac{t_0}{2} + t_0 + t_b = \frac{T_s}{4} + K.V^* \left[ \sin\left(\frac{\pi}{3} - \alpha^*\right) + \sin(\alpha^*) \right], S = 3,4 \\ \frac{t_0}{2} + t_a = \frac{T_s}{4} + K.V^* \left[ \sin\left(\frac{\pi}{3} - \alpha^*\right) - \sin(\alpha^*) \right], S = 5 \end{cases}$$

Equation (4) can be written in the general form

$$T_{A=ON} = T_s/4 + f(V^*)g_A(\alpha^*)$$

Where  $f(V^*)$  is the voltage amplitude scale factor and

$$g_A(\alpha^*) = \begin{cases} K \left[ -\sin\left(\frac{\pi}{3} - \alpha^*\right) - \sin(\alpha^*) \right], S = 1,6 \\ K \left[ -\sin\left(\frac{\pi}{3} - \alpha^*\right) + \sin(\alpha^*) \right], S = 2 \\ K \left[ \sin\left(\frac{\pi}{3} - \alpha^*\right) + \sin(\alpha^*) \right], S = 3,4 \\ K \left[ \sin\left(\frac{\pi}{3} - \alpha^*\right) - \sin(\alpha^*) \right], S = 5 \end{cases}$$

Fig.(3) shows schematic of the ANN based SVPWM inverter. The input signal to the neural network is angle  $\theta$ . Fig.(4) shows the neural network model. The model uses a multilayer function in the first and second layer respectively. The Neural network uses one neuron at the input 25 neurons in the hidden layer and three output neurons. The digital words corresponding to turn on time are generated by multiplying the output of neural network with  $V^*T_s$  and then adding  $T_s/4$  as shown in figure. The PWM signals are then generated by comparing turn-On time with a triangular reference having time period of  $T_s$  and amplitude  $T_s/2$ .the PWM signals are then applied to the inverter. The back propagation algorithm in the Matlab toolbox is used for the training. The angle subnet takes 101650 epochs for training with an error 0.53%.The value of size and the corresponding training time are, thus reasonably small.

### IV. DRIVE SYSTEM PARAMETERS

DC-link voltage	300 V
Sampling time ( $T_s$ )	200 $\mu$ s
Induction motor	5 hp 230 V four pole, squirrel cage
Frequency range:	0-50 Hz

Stator resistance ( $R_s$ ):	0.5814 $\Omega$
Rotor resistance ( $R_r$ ):	0.4165 $\Omega$
Stator leakage inductance ( $L_{ls}$ ):	3.479 mH
Rotor leakage inductance ( $L_{lr}$ ):	4.15 mH
Mutual inductance ( $L_m$ ):	78.25mH
Rotor inertia (J):	0.1 kg.m <sup>2</sup>

### V. SIMULINK MODEL AND RESULTS

The Simulink model used to simulate model results on computer is shown in fig. (7).Fig. (8) Shows the characteristics of speed of induction motor. In the starting speed of motor increases very sharply and reaches to 1700 rpm after 0.1 seconds. Some oscillations are followed till the motor catches the reference speed (i.e. 1500 rpm) in 0.3 seconds. At 0.75 seconds when load torque is changed from 0 Nm to 10 Nm it attains a value slightly lower than the reference speed as shown in the figure. This lower value of speed is due to increase in torque and the system is operating in open loop. Fig.(9) shows torque characteristics for load torque (Tl) and developed torque (Te). In the starting the applied torque is zero and developed torque is very high for few seconds (till 0.125 second). After some fluctuation it attains a value of zero in 0.25 seconds, as Tl=0. When load torque is increased to a value 10 Nm at 0.75 seconds. The developed torque also attains the value of 10 Nm following some transients as depicted in figure. The stator and rotor current for dynamic conditions are shown in fig.(10) &(11) respectively. The current shows the expected trend and are sinusoidal in nature. These currents have high initial values during starting due to starting transients, as the induced emf takes time to develop its rated value. After these transients are over, the current settles at its steady state values after 0.2 sec. The currents (stator and rotor) get higher values when load torque of 10 Nm is applied at 0.75 sec. These can be seen in the figure.

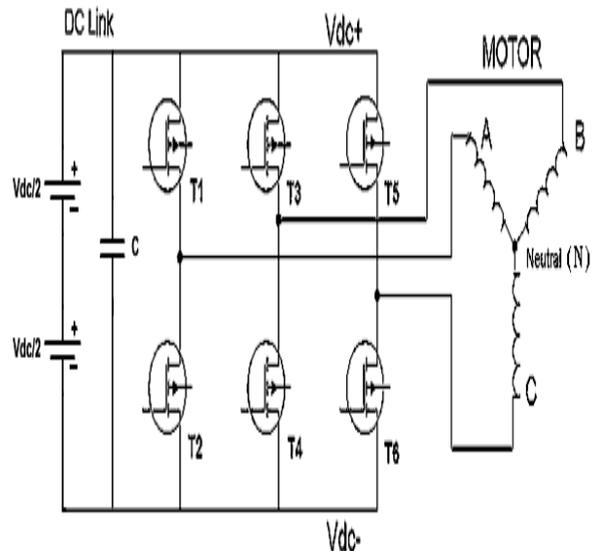


Fig. 1. Power circuit of a three-phase voltage source inverter

[12]

Table-1 possible modes of operation of a three-phase VSI [12]

State	On Devices	V <sub>an</sub>	V <sub>bn</sub>	V <sub>cn</sub>	Space Voltage Vector
0	T <sub>2</sub> , T <sub>4</sub> , T <sub>6</sub>	0	0	0	V <sub>0</sub> (000)
1	T <sub>1</sub> , T <sub>4</sub> , T <sub>6</sub>	2V <sub>dc</sub> /3	-V <sub>dc</sub> /3	-V <sub>dc</sub> /3	V <sub>1</sub> (100)
2	T <sub>1</sub> , T <sub>3</sub> , T <sub>6</sub>	V <sub>dc</sub> /3	V <sub>dc</sub> /3	-2V <sub>dc</sub> /3	V <sub>2</sub> (110)
3	T <sub>3</sub> , T <sub>2</sub> , T <sub>6</sub>	-V <sub>dc</sub> /3	2V <sub>dc</sub> /3	-V <sub>dc</sub> /3	V <sub>3</sub> (010)
4	T <sub>2</sub> , T <sub>3</sub> , T <sub>5</sub>	-2V <sub>dc</sub> /3	V <sub>dc</sub> /3	V <sub>dc</sub> /3	V <sub>4</sub> (011)
5	T <sub>2</sub> , T <sub>4</sub> , T <sub>5</sub>	-V <sub>dc</sub> /3	-V <sub>dc</sub> /3	2V <sub>dc</sub> /3	V <sub>5</sub> (001)
6	T <sub>1</sub> , T <sub>4</sub> , T <sub>5</sub>	V <sub>dc</sub> /3	-2V <sub>dc</sub> /3	V <sub>dc</sub> /3	V <sub>6</sub> (101)
7	T <sub>1</sub> , T <sub>3</sub> , T <sub>5</sub>	0	0	0	V <sub>7</sub> (111)

Table 2 – Application of time [13]

Sector I (0 ≤ ωt ≤ π/3)	Sector II (π/3 ≤ ωt ≤ 2π/3)	Sector III (2π/3 ≤ ωt ≤ π)
T <sub>1</sub> = √3/2 mT <sub>s</sub> cos(ωt + π/6)	T <sub>2</sub> = √3/2 mT <sub>s</sub> cos(ωt + 11π/6)	T <sub>3</sub> = √3/2 mT <sub>s</sub> cos(ωt + 3π/2)
T <sub>2</sub> = √3/2 mT <sub>s</sub> cos(ωt + 3π/2)	T <sub>3</sub> = √3/2 mT <sub>s</sub> cos(ωt + 7π/6)	T <sub>4</sub> = √3/2 mT <sub>s</sub> cos(ωt + 5π/6)
T <sub>0</sub> + T <sub>7</sub> = T <sub>s</sub> - T <sub>1</sub> - T <sub>2</sub>	T <sub>0</sub> + T <sub>7</sub> = T <sub>s</sub> - T <sub>2</sub> - T <sub>3</sub>	T <sub>0</sub> + T <sub>7</sub> = T <sub>s</sub> - T <sub>3</sub> - T <sub>4</sub>
Sector IV (π ≤ ωt ≤ 4π/3)	Sector V (4π/3 ≤ ωt ≤ 5π/3)	Sector VI (5π/3 ≤ ωt ≤ 2π)
T <sub>4</sub> = √3/2 mT <sub>s</sub> cos(ωt + 7π/6)	T <sub>5</sub> = √3/2 mT <sub>s</sub> cos(ωt + 5π/6)	T <sub>6</sub> = √3/2 mT <sub>s</sub> cos(ωt + π/2)
T <sub>5</sub> = √3/2 mT <sub>s</sub> cos(ωt + π/2)	T <sub>6</sub> = √3/2 mT <sub>s</sub> cos(ωt + π/6)	T <sub>1</sub> = √3/2 mT <sub>s</sub> cos(ωt + 11π/6)
T <sub>0</sub> + T <sub>7</sub> = T <sub>s</sub> - T <sub>4</sub> - T <sub>5</sub>	T <sub>0</sub> + T <sub>7</sub> = T <sub>s</sub> - T <sub>5</sub> - T <sub>6</sub>	T <sub>0</sub> + T <sub>7</sub> = T <sub>s</sub> - T <sub>1</sub> - T <sub>6</sub>

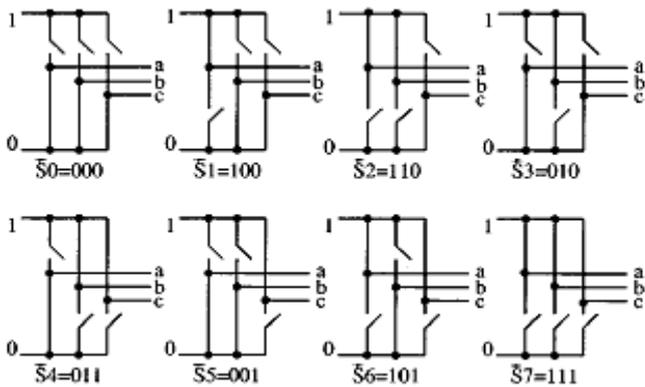


Fig. 2 -The switches position during eight topologies [12]

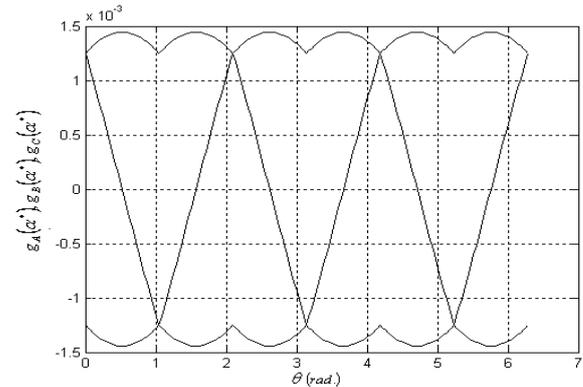


Fig.5 Turn-on pulse width function of phase A, B, C as a function of Angle θ in different sectors [13]

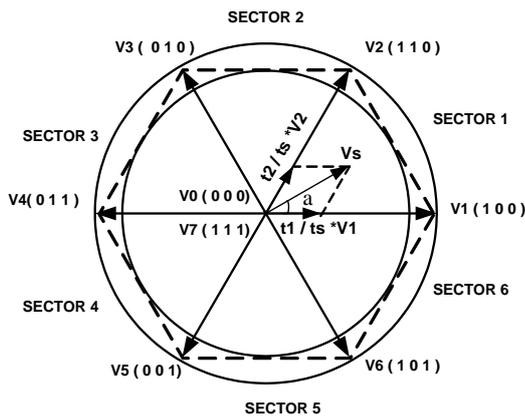


Fig. 3 - Space Vector representation of Line to Neutral Voltages [13]

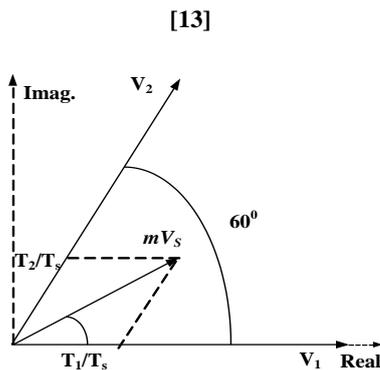


Fig. 4- Principle of time calculation for SVPWM in sector I [13]

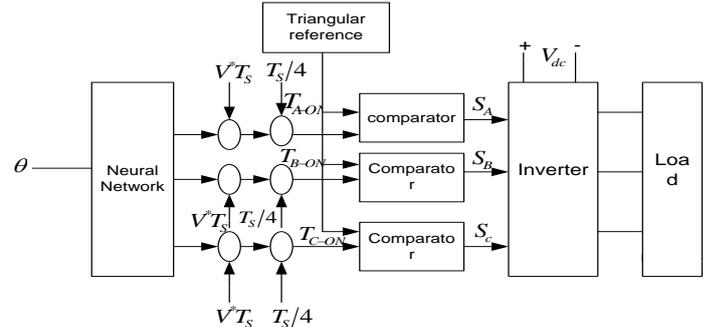


Fig.6 Schematic of the ANN based SVPWM inverter [5]

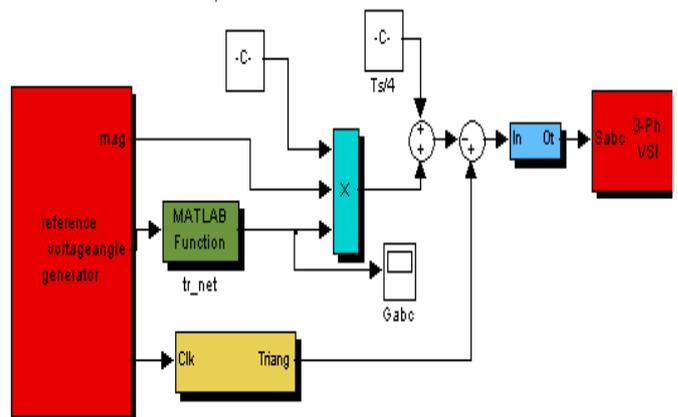


Fig.7 Model of Ann based space vector PWM controller for voltage Fed inverter Induction motor drive

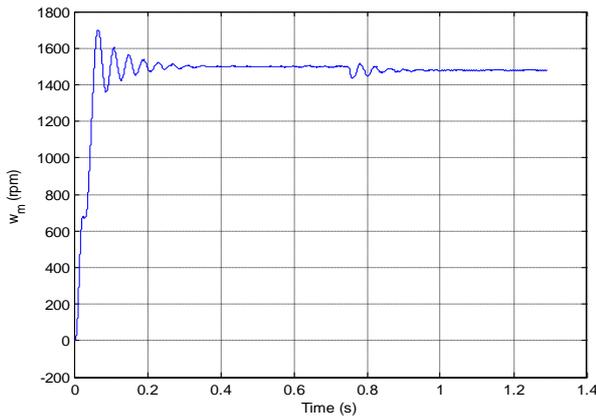


Fig.8 Speed characteristics

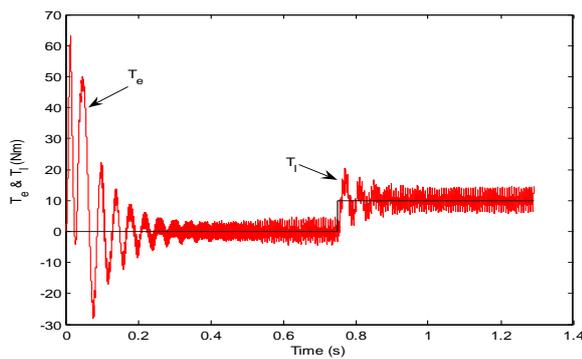


Fig.9 Torque characteristics

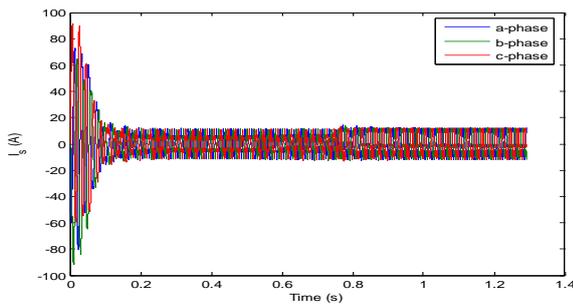


Fig.10 Stator current waveform

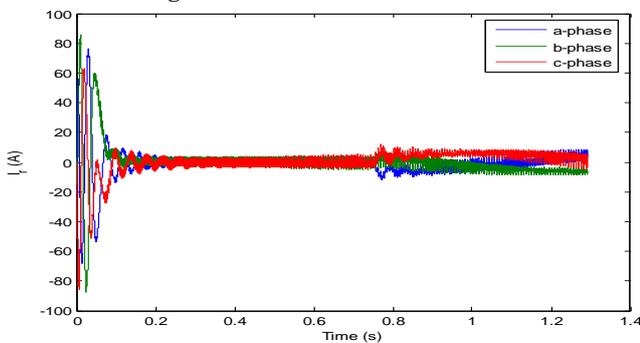


Fig.11 Rotor current waveform

## VI. CONCLUSION

A neural-network-based space-vector modulator has been described that operates very well in under modulation region. The digital words corresponding to turn-on time are generated by the ANN and then converted to pulse widths through a single timer. The scheme uses a back

propagation-type feed forward network. The training data and training time are reasonably small. The method can operate from dc (zero frequency). The scheme has been fully implemented and evaluated with a V/Hz-controlled induction motor drive, and gives excellent performance. The PWM controller is currently being used in a stator-flux-oriented vector-controlled induction motor drive. The ANN-based SVM can give higher switching frequency, which is not possible by conventional DSP-based SVM. The switching frequency can be easily extended up to 50 kHz if the ANN is implemented by a dedicated hardware ASIC chip.

## VII. FUTURE ENHANCEMENT OF WORK

The ANN-based SVM can give higher switching frequency, which is not possible by conventional DSP-based SVM. The switching frequency can be easily extended up to 50 kHz if the ANN is implemented by a dedicated hardware ASIC chip.

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